

Datasheet :

Data Logger & Internet Gateway

3000X Series

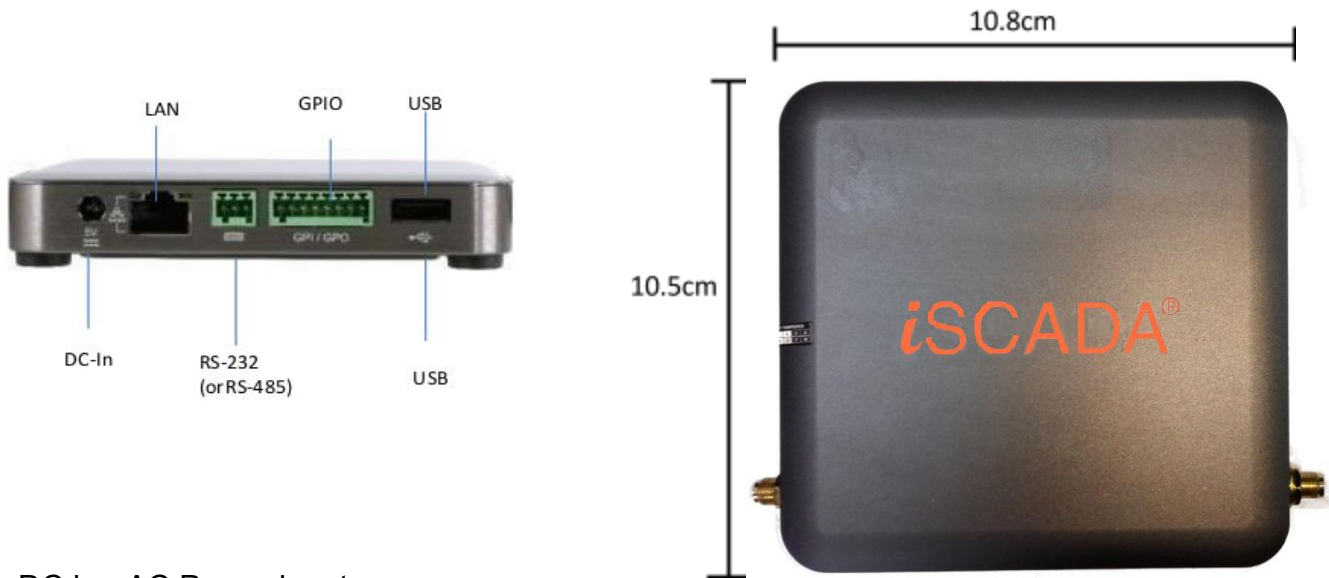
Product Specifications

System on Chip	Intel® Quark SoC X1021, 400MHz, 16KB L1 Cache
Flash	8MB NVRAM SPI NOR Flash Chip
RAM	1GB (DDR3L 512MB x 2)
Power	5VDC@3A (without PCIe Module)
External I/O	DC 5V In 10/100Mb Ethernet Port x 1 USB Host (USB 2.0) x 1 RS-485 (Half-Duplex) x 1 General Purpose Input (5V TTL Level) x 4 General Purpose Output (5V TTL Level) x 4 3G Antenna Port x 2
Internal I/O	Mini PCIe (Full) x 1 Mini PCIe (Half) x 1
Field Communication	Modbus/RTU
Remote Upgrade	Yes
Time Sync	Internet (Auto / Manual)
Operating Temperature	0°C to 50°C
Operating Humidity	10% to 95% @ 40°C Non Condensing
LED Indicator	Power On / Status
Buttons	Reset General Purpose
EMC Certifications	CE, FCC, BSMI, NCC
Safety Certifications	UL, CB
Dimension	108 x 104 x 20.5 mm

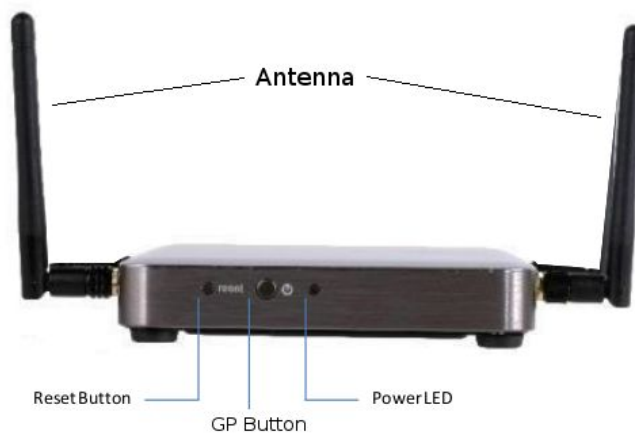
The Gateway 3000X Series is centred around the Intel® Quark SoC X1021. The Quark’s intricately integrated System on Chip construction provides high functionality with low power consumption, allowing 3000X devices to deliver significant savings without compromising performance. As in the preceding generation, the 3000X boasts an absence of mechanical parts along with all the reliability increases implied by such design.

The Gateway 3000X Series features up to 1GB of built-in RAM capacity supported by an 8MB NVRAM SPI NOR Flash Chip.

Mechanical Drawings

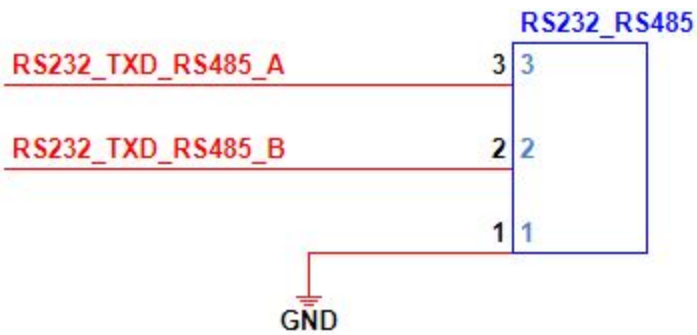
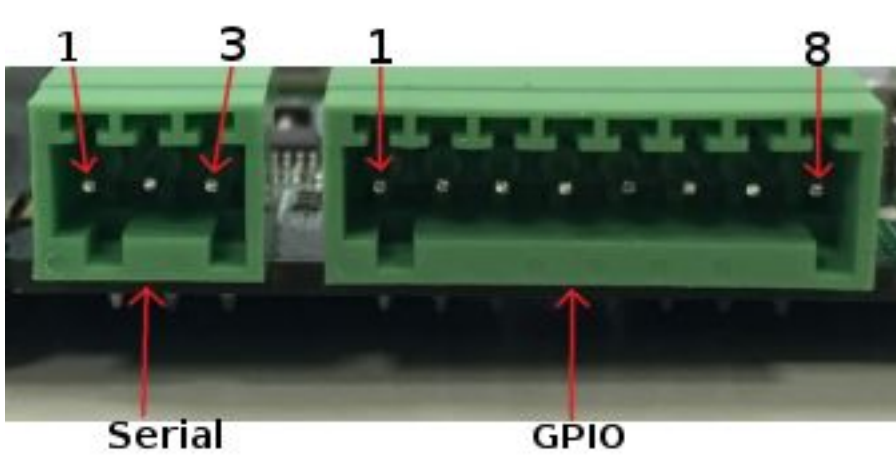


- 1) DC in - AC Power input
- 2) Serial - RS485 or 232
- 3) LAN - RJ45 Ethernet connector
- 4) GPIO - Consists 4 digital input and 4 digital output
- 5) Reset button - to warm boot the gateway
- 6) GP Button - Global purpose button (multi functional)
- 7) Power LED - Indicator for the hardware status
- 8) Antenna specification:
 - a) Frequency Range: 824Mhz - 960Mhz, 1710Mhz - 2170Mhz
 - b) Impedance: 50 ohm
 - c) VSWR: 4.5 Max



Wiring Guide

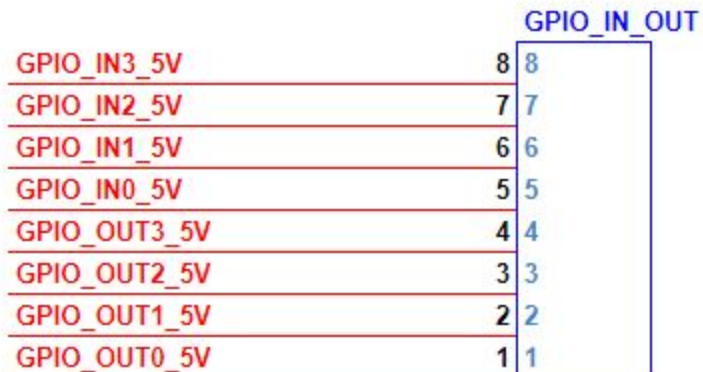
1) Serial Pin



RS-323	
PIN 1	GND
PIN 2	RX
PIN 3	TX

RS-485	
PIN 1	GND
PIN 2	B(-)
PIN 3	A(+)

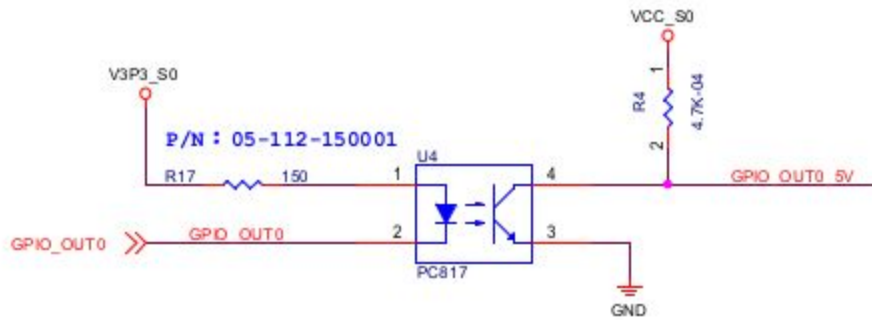
2) GPIO Pin



GPIO_IN_OUT (5V)	
PIN 1	GPIO_OUT0
PIN 2	GPIO_OUT1
PIN 3	GPIO_OUT2
PIN 4	GPIO_OUT3
PIN 5	GPIO_IN0
PIN 6	GPIO_IN1
PIN 7	GPIO_IN2
PIN 8	GPIO_IN3

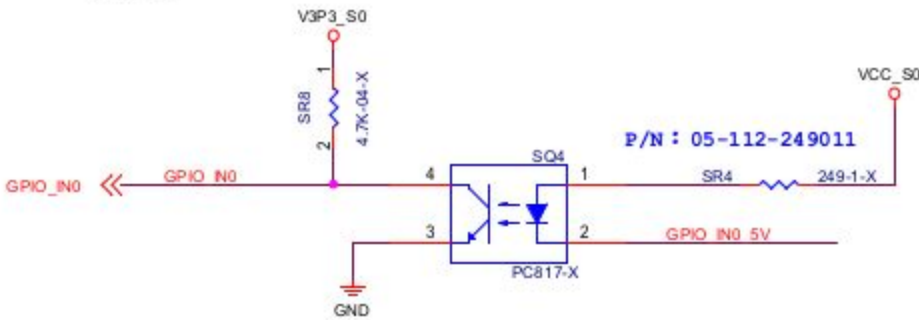
a) Global purpose output internal connection circuitry:

GPO



b) Global purpose input internal connection circuitry

GPI



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